

Class	Subclass

ISSUE CLASSIFICATION:

PATENT NUMBER

U.S. **UTILITY** Patent Application

**O.I.P.E.**

**PATENT DATE**

APPLICATION NO. 09/823159	CONT/PRIOR	CLASS 710	SUBCLASS 129	ART UNIT 2112	EXAMINER PATEL
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## APPLICANTS

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**TITLE**

### Programmable CPU/interface buffer structure using dual port RAM

**APPLICANT(S):**

PTO-2040  
12/99

ISSUING CLASSIFICATION												
ORIGINAL				CROSS REFERENCE(S)								
CLASS		SUBCLASS		CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)							
INTERNATIONAL CLASSIFICATION												

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<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>		<b>CLAIMS ALLOWED</b>	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner)		<b>NOTICE OF ALLOWANCE MAILED</b>  _____	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____  _____  _____	_____ (Primary Examiner)		<b>ISSUE FEE</b>	
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<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner)		<b>ISSUE BATCH NUMBER</b>	
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